Dr GOPAVARAM SUNEEL KUMAR,
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Career Objective:

- Seeking a challenging position that has high growth opportunities; a firm that prides itself on innovation, growth and excellence.

Educational Qualification:

- Completed **S.S.C** from the **Central Board of Secondary Education** in the year 1999 with an aggregate of 76.00%.
- Completed **Intermediate** from the **Board of Intermediate Education,** Andhra Pradesh in the year 1999-2001 with an aggregate of 88.50%.
- Graduated as Bachelor of Technology in the stream of **Electronics & Communication Engineering** from **KSRM College of Engineering** under S.V. University, Tirupathi in the year 2001-2005 with an aggregate of 74%.
- Post Graduated as Master of Technology in the stream of **Electronics & Communication Engineering** with a specialization in **VLSI System Design** from **St. John's Engineering College** under J.N.T.U.A, Ananthapuramu in the year 2010-2012 with an aggregate of 76%.
- **Completed PhD** at **J.N.T.U.A., Ananthapuramu** in the department of **Electronics & Communication Engineering** in the area of **VLSI** in the year 2023.

Professional Experience:

- Having a teaching experience of 6 months in KSRM College of Engineering, Kadapa as an Associate Professor from April 2024 to till date.
- Having a total teaching experience of 18 years 4 months in KSRM College of Engineering, Kadapa as an Assistant Professor from December 2005 to March 2024.

Publications:

Journals:

- D Anusha, Dr G Suneel Kumar, An investigation of a Low Power 10T SRAM 8X8 array using the Multi-threshold and Variable-threshold CMOS technique, Journal of Information and Computational Science, September 2024(UGC-Care Group-II Journal)
- Gopavaram Suneel Kumar, Dr G Mamatha, Ultra-Low Power 5T-SRAM Cell Design using different CNTFETs for exploring Read/Write Assist Techniques, International Journal of Engineering Trends and Technology, vol. 19, Issue 1, 10.22068/IJEEE.19.1.2699, March 2023, http://ijeee.iust.ac.ir/article-1-2699-en.html (SCOPUS)
- Gopavaram Suneel Kumar, Dr G Mamatha, Improved Read/Write Stability based Level Shift 5T Ternary SRAM Cell Design using Enhanced Gate Diffusion Input BWGCNTFET, Journal of Circuits, Systems and Computers, World Scientific Publications, July 2022, https://doi.org/10.1142/S0218126623500032 (SCI)

Conferences:

- Dr G Suneel Kumar, A Systematic mobile application for students data maintenance using Visual Studio, International Conference on Communications and Cyber-Physical Engineering (ICCCE), July 19-20,2024(Scopus Indexed)
- Dr G Suneel Kumar, Online Results Portal, International Conference on Communications and Cyber-Physical Engineering (ICCCE), July 19-20,2024(Scopus Indexed)
- Dr G Suneel Kumar, An Optimized Occasion Crafters Web Application using MERN Stack, International Conference on Communications and Cyber-Physical Engineering (ICCCE), July 19-20,2024(Scopus Indexed)
- Gopavaram Suneel Kumar, Dr G Mamatha, An Efficient 5-Transistor SRAM Cell Design using FNSBS-CNTFET for Improving Read and Write Stability, IEEE International Conference on Data Science and Information System (ICDSIS), July 2022

FDPs/STTPs:

- A Five-day Faculty Development Program on "Design Thinking and Innovation" from 18th to 22nd November 2024
- One week FDP on "Java Demystified" from 3rd to 9th October 2024
- A Five-day Online Faculty Development Program on "The Future of Computing: Emerging Technologies & Trends" from 8th to 12th July 2024
- One week FDP on "Applications of atmospheric Remote Sensing & GIS" from 25th to 29th July 2022
- A Five-day National Level Online FDP on " Applications of Signal Processing and Computer Vision Using MATLAB and SIMULINK" from 7th to 11th March 2022
- One week FDP on "Research Methodology" from 17th to 21st January 2022
- Online FDP on "Android Application Development using Kotlin" from 27th October to 10th November 2021
- One week FDP on Deep learning: perspectives, Trends and Research Prospects" from 25th to 30th October 2021
- One week Online STTP on "Internet of Things[Iot] Machine Learning[ML]" from 20th to 25th September 2021
- One week Online National Level FDP on "Computational Intelligence & Its Applications" from 16th to 21st August 2021
- One week FDP on "Advanced Digital Signal Processing & Its Applications" from 20th to 26th November 2017

Certifications:

- Cloud Computing (NPTEL-CS17)
- Switching Circuits and Logic Design (NPTEL-CS10)
- Computer Architecture (NPTEL-CS83)
- Introduction to Industry 4.0 & Industrial Internet of Things (NPTEL-CS95)
- Introduction to Internet of Things (NPTEL-CS115)
- LabVIEW CLAD Associate (National Instruments)

Declaration:

I hereby declare that the information and facts stated above are correct and true to the best of my knowledge and belief.

Place: Kadapa
Date: 09-12-24 **SUNEEL KUMAR G**